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(54) MPEG DATA PROCESSING CIRCUIT

(57) Abstract:

PROBLEM TO BE SOLVED: To provide a moving picture expert group(MPEG) data processing circuit with which a system also corresponding to an MPEG2 program stream(PS) can be easily constructed without changing the basic circuit configuration of an MPEG data processing circuit prepared for processing an MPEG2 transport stream(TS).

SOLUTION: The MPEG2-TS is fetched from a host bus by a host bus control part 1, stored through a selector 7 into a first-in/first-out FIFO 8 and converted later to video and audio signals by a TS separating part 9 and an A/V decoder 10. In the case of MPEG2-PS, it is fetched from the host bus, afterwards, a packetized elementary stream(PES) and a system clock reference(SCR) are separated by a PS separating part 2, the separated PES and a program clock reference(PCR) generated by a PCR generating part 5 are reprogrammed into MPEG2-TS by a TS programming part 3, stored in the FIFO 8 and converted to video and audio signals by the TS separating part 9 and the A/V decoder 10.

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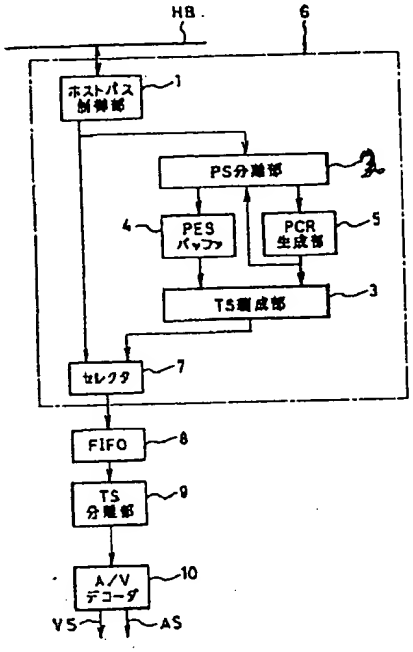

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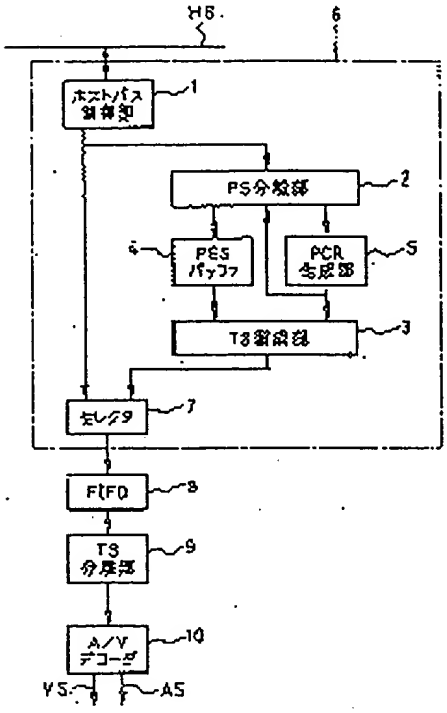
The processor consists of a PS separation unit (2) to separate PES (Packetized Elementary Stream) and system clock reference from MPEG2 program stream. A PES buffer (4) holds the separated PES. A PCR generator (5) counts and generates PCR based on the amount of data and the rate of forwarding data. A TS organisation unit (3) reads the PES and the PCR, to generate MPEG2 transport stream.

ADVANTAGE - Generates value of PCR correctly. Assembles MPEG decoder corresponding to MPEG2 transport stream and MPEG2 program stream, easily. (Dwg.1/8)


H5
6
1
PS分離部
2
PES
バッファ
4
PCR
生成部
5
TS組成部
3
セレクタ
7
FIFO
8
TS
分離部
9
A/V
デコーダ
10
V5
A5